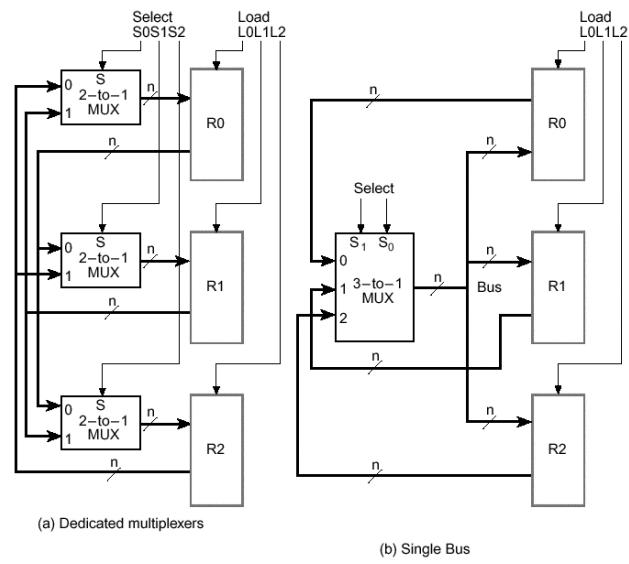


Bus versus Dedicated Multiplexer

- Multiplexer

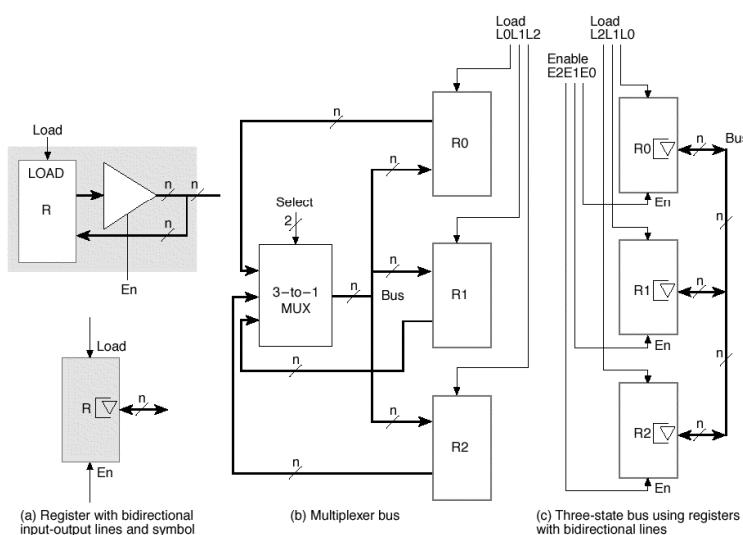


Flaxer Eli - Logic Design

Ch 11 - 1

Three State Bus

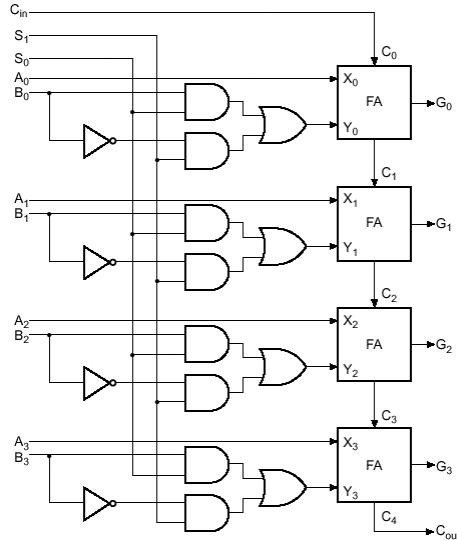
- Three State



Flaxer Eli - Logic Design

Ch 11 - 2

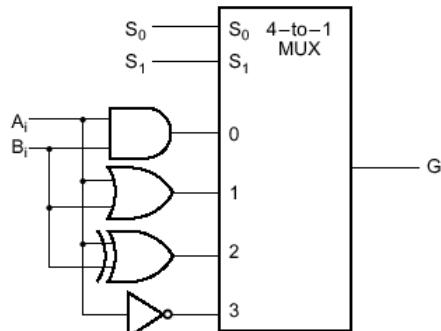
Arithmetic Microoperation



Flaxer Eli - Logic Design

Ch 11 - 3

Logic Microoperation



(a) Logic Diagram

S_1	S_0	Output	Operation
0	0	$G = A \wedge B$	AND
0	1	$G = A \vee B$	OR
1	0	$G = A \oplus B$	XOR
1	1	$G = \bar{A}$	NOT

(b) Function Table

Flaxer Eli - Logic Design

Ch 11 - 4